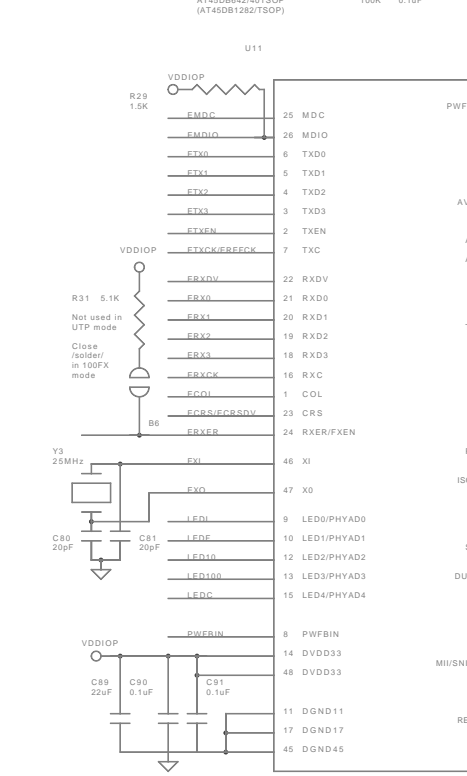
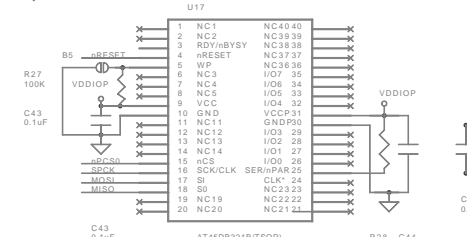
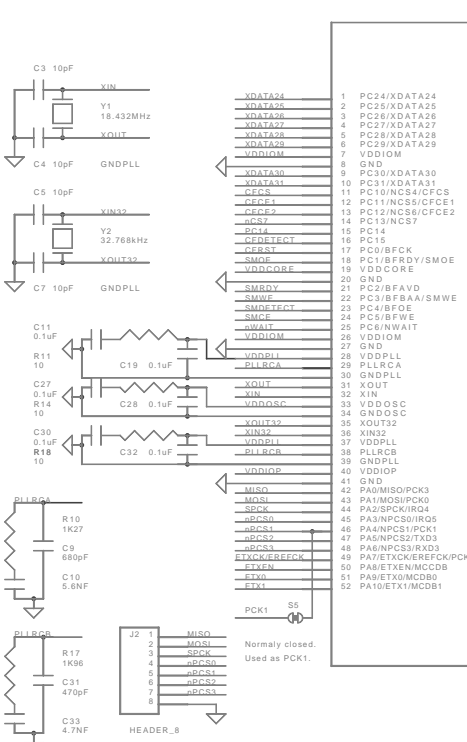
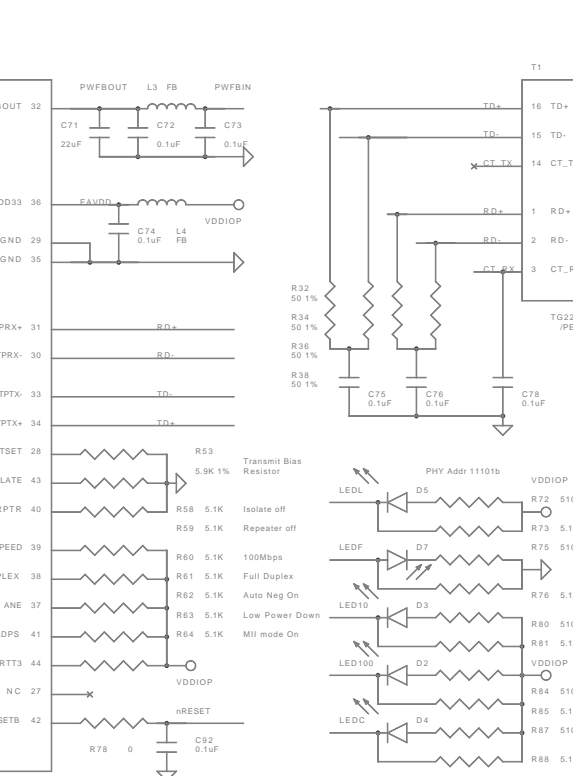
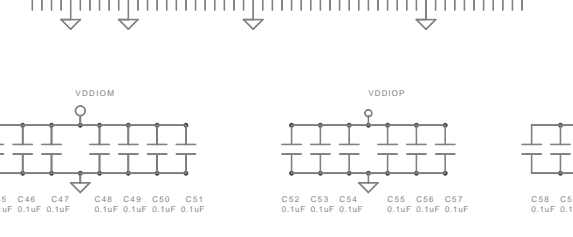
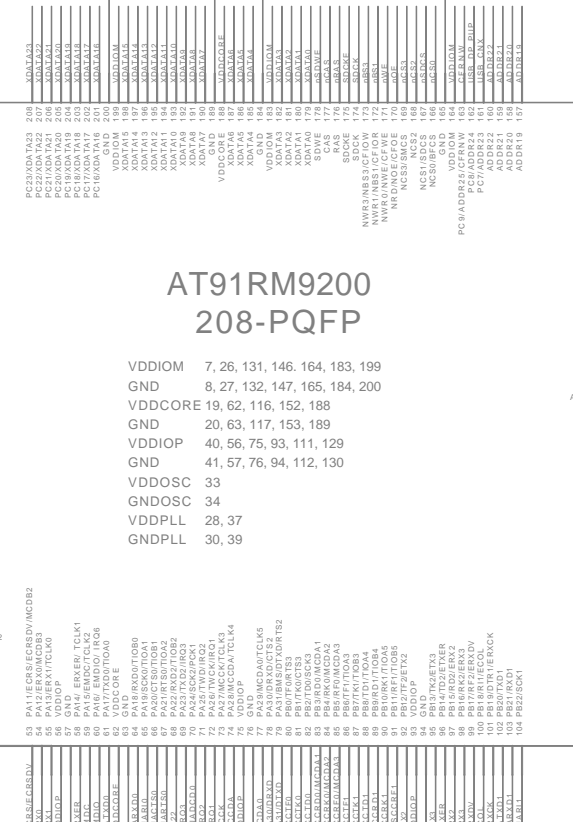


Port C Pins assignment

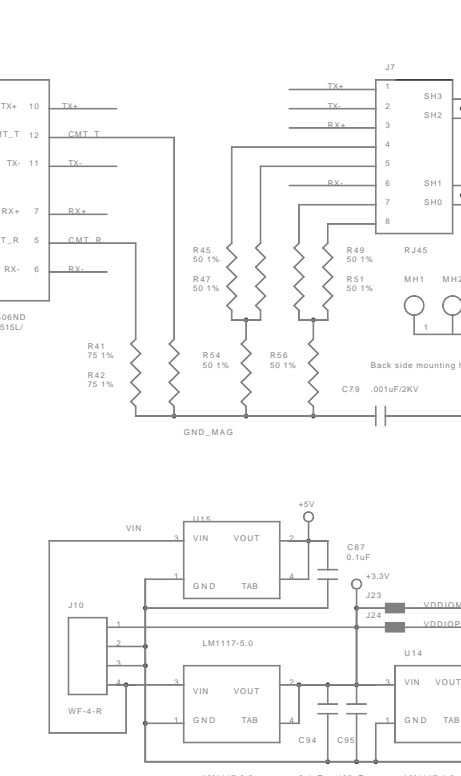
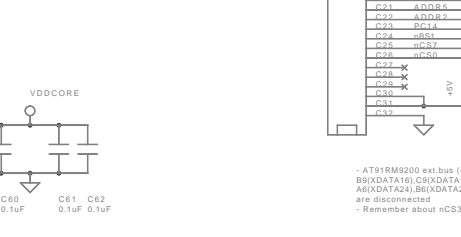
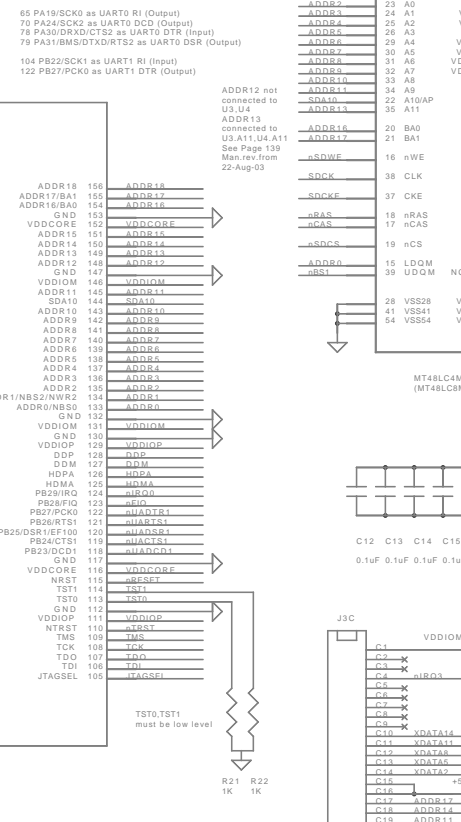
- 17 PC15 as CFDETECT (GP I/O)
- 16 PC10 as CFRST (GP I/O)
- 18 PC1 in SMOE mode
- 21 PC2 as SMRDY (GP I/O)
- 22 PC3 in SMWE mode
- 23 PC4 as SMDETECT (GP I/O)
- 24 PC5 as SMCE (GP I/O)
- 163 PC9 in CFRNW mode
- 161 PC7/A23 as UCB_CNXP
- 162 PC8/A24 as UCB_DP_PUP



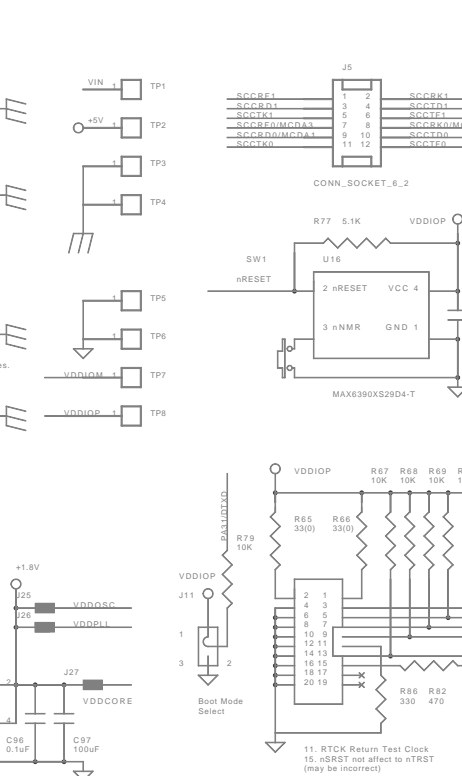
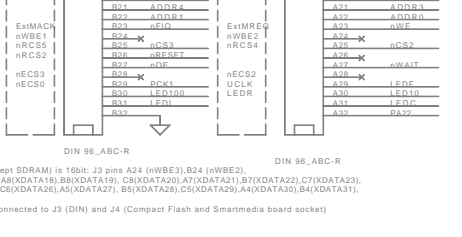
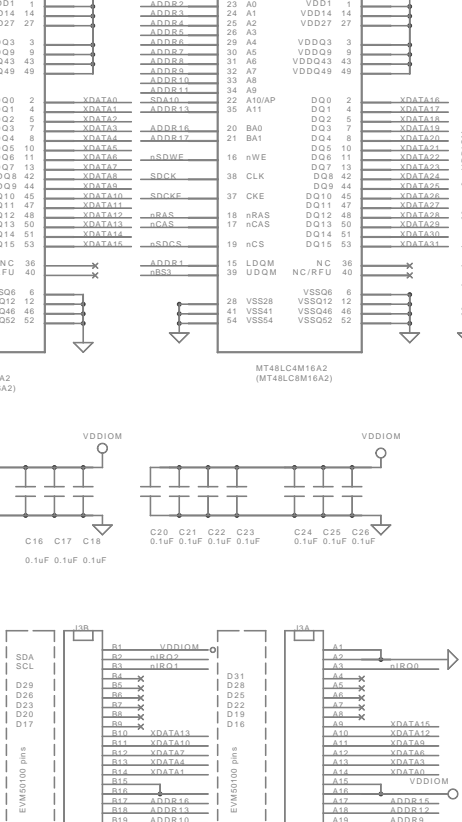
GP I/O Pins as UART0/UART1/UART2/UART3



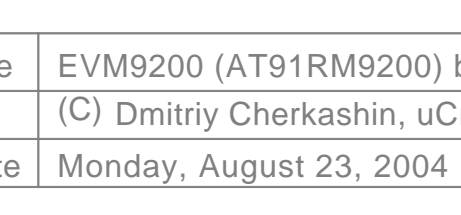
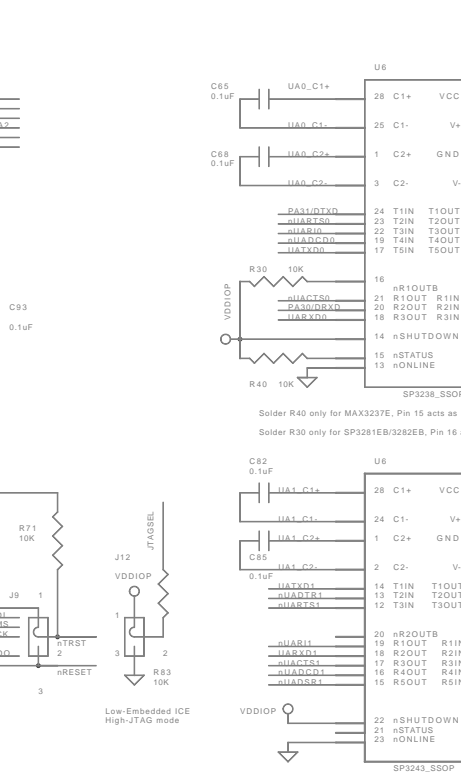
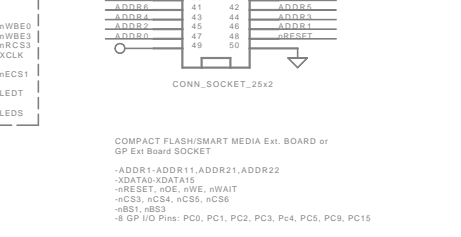
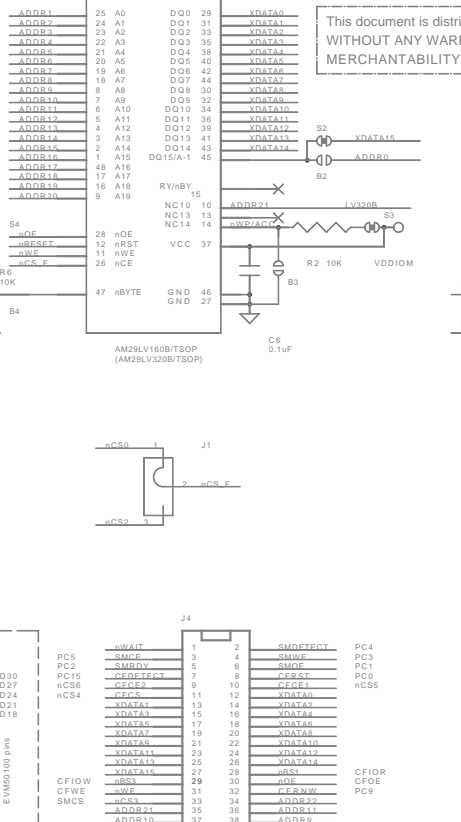
GP I/O Pins as UART0/UART1/UART2/UART3



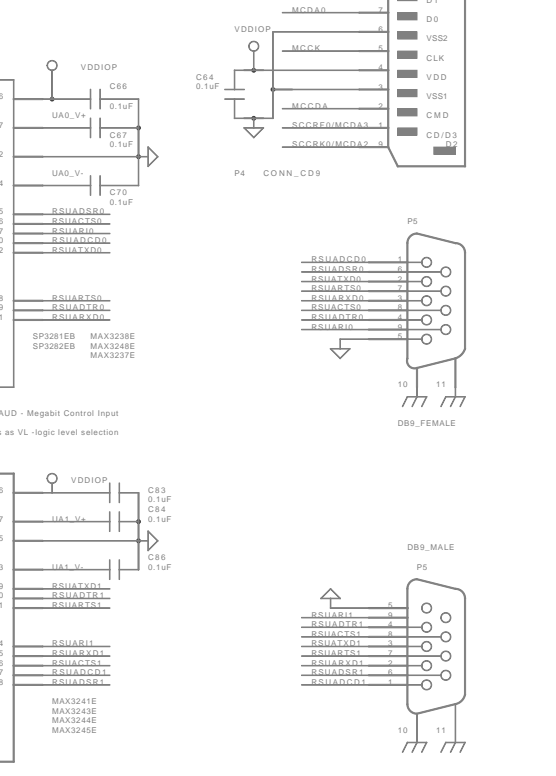
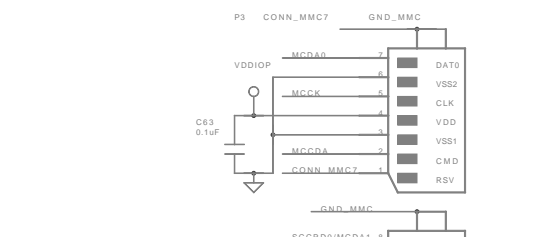
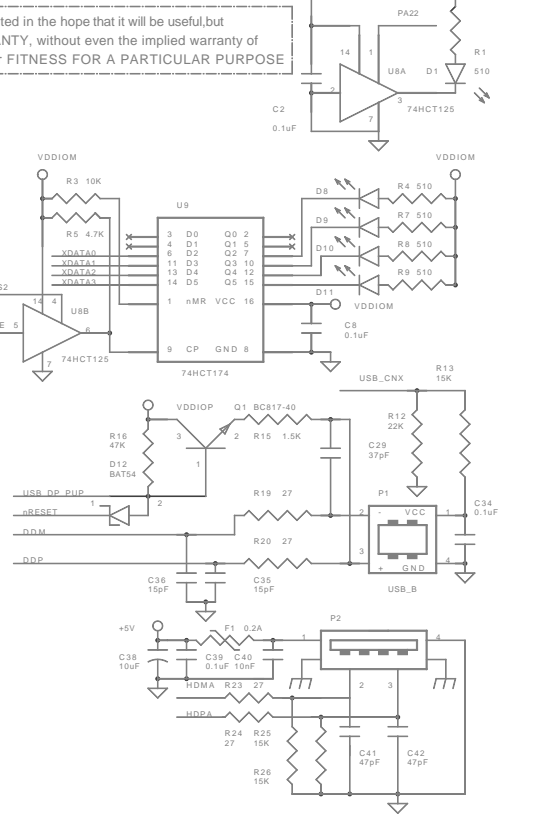
GP I/O Pins as UART0/UART1/UART2/UART3



GP I/O Pins as UART0/UART1/UART2/UART3



GP I/O Pins as UART0/UART1/UART2/UART3



AT91RM9200 208-PQFP

- VDDIOM 7, 26, 131, 146, 164, 183, 199
- GND 8, 27, 132, 147, 165, 184, 200
- VDDCORE 19, 62, 116, 152, 188
- GND 20, 63, 117, 153, 189
- VDDIOP 29, 60, 56, 75, 93, 111, 129
- GND 41, 57, 76, 94, 112, 130
- VDDOSC 33
- VDDOSC 34
- VDDPLL 28, 37
- GNDPLL 30, 39

TS10, TS11 must be low level

AT91RM9200 bit bus (except SDRAM) is 16bit; J3 pins A24 (NWBES), B24 (HWBE2), B8(XDATA16), C9(XDATA17), A8(XDATA18), B8(XDATA19), C8(XDATA20), A7(XDATA21), B7(XDATA22), C7(XDATA23), A6(XDATA24), B6(XDATA25), C6(XDATA26), A5(XDATA27), C5(XDATA28), C5(XDATA29), A4(XDATA30), B4(XDATA31), are disconnected

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Title	EVM9200 (AT91RM9200) board	Rev 1A
A3	(C) Dmitry Cherkashin, uCrouter project	(www.ucrouter.ru)
Date	Monday, August 23, 2004	