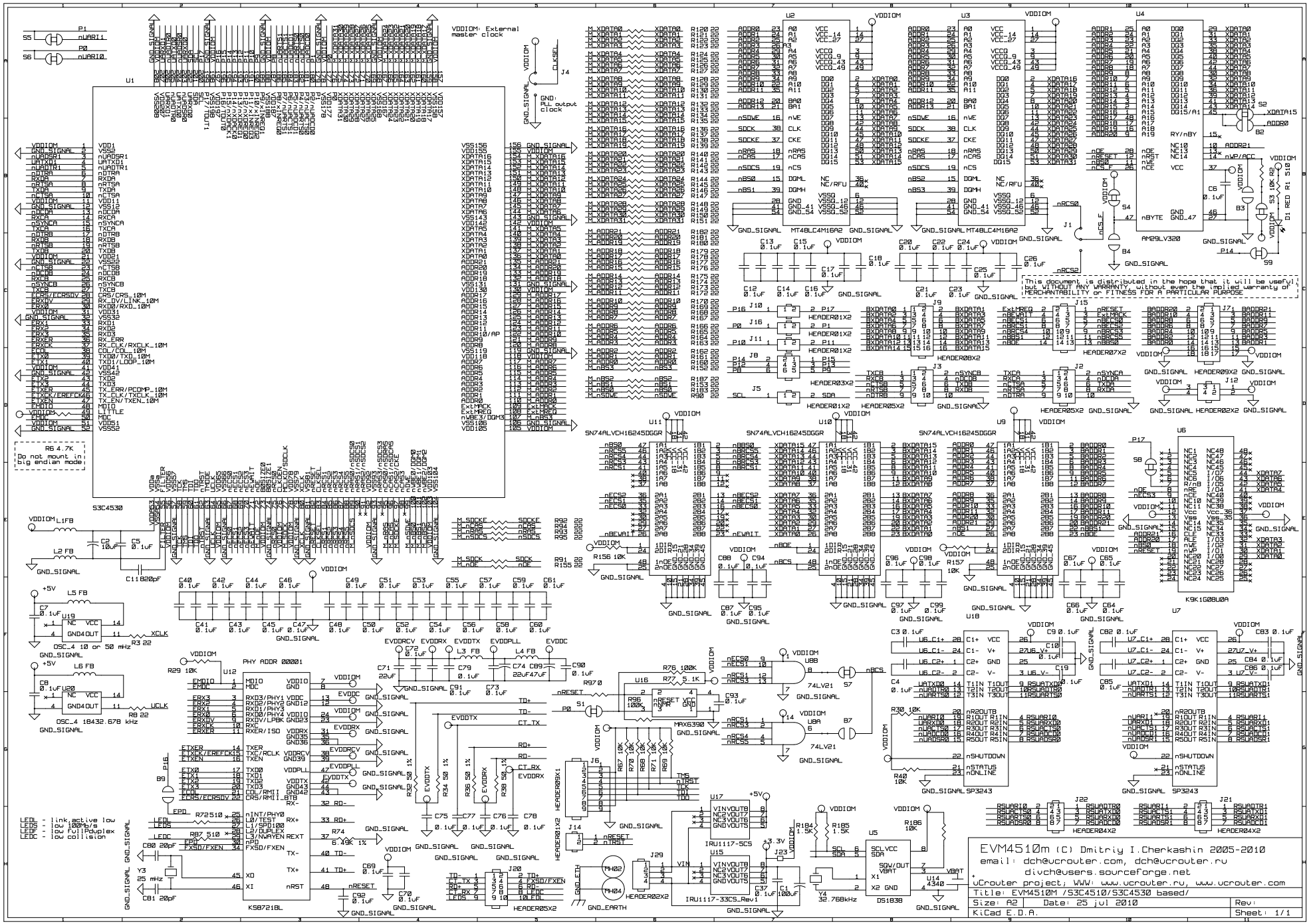

VDDIOM: External master clock
GND: PLL output clock
R6 4.7K
Do not mount in big endian mode
S3C4530
PHY ADDR 00001
KS8721BL
MT48LC4M16A2
AM29LV320
SN74ALVCH16245DGGR
K9K1G08U0A
MAX6390
74LV21
SP3243
IRU1117-5CS
IRU1117-33CS_Rev1
DS1838
OSC_4 10 or 50 mHz
OSC_4 18432.678 kHz
LEDL - link,active low
LEDS - low 100Mb/s
LEDF - low fullPduplex
LEDC - low collision
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EVM4510m (C) Dmitriy I.Cherkashin 2005-2010
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uCrouter project; WWW: www.ucrouter.ru, www.ucrouter.com
Title: EVM4510M /S3C4510/S3C4530 based/
Size: A2
Date: 25 jul 2010
Rev:
KiCad E.D.A.
Sheet: 1/1